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U.S. UTILITY Patent Application

**PATENT NUMBER and
ISSUE DATE**

897

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10053489	11/02/2001	257	678	2814	P. CAO

****APPLICANTS:** Pasqualini Ronald;

****CONTINUING DATA VERIFIED:**

None, R

**** FOREIGN APPLICATIONS VERIFIED:**

None, R

PG-PUB DO NOT PUBLISH

RESCIND ☐

Foreign priority claimed
35 USC 119 conditions met

☐ yes ☒ no
☐ yes ☒ no

ATTORNEY DOCKET NO

NSC1P218/P05063

Verified and Acknowledged Examiners's initials

PC

TITLE : Integrated circuit package with low inductance ground path and improved thermal capability

U.S. DEPT. OF COM. PAT. & TM. PTO 436, Rev. 10/94

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner	DRAWING	
Amount Due	Date Paid		Sheets Drwg.	Figs. Drwg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner	
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